



+5 to +12 VDC to pin 14 of each IC
 Ground to pin 7 of each IC

IC A must be a CD4011 or eq
 IC B may be a CD4011 or any
 other CMOS gate that will
 provide an inverter function

Capicators can be 25 volt
 Resistors are 1/4 watt

A 0.01 capacitor may be paralleled with
 the 330 K resistors to provide de-emphasis
 for each audio path.

Pin Functions

1. Secondary Discriminator
2. Secondary Carrier Operated Squelch
3. Primary Carrier Operated Squelch
4. Combined Carrier Operated Squelch Output
5. Primary Discriminator
6. Combined Audio Output
7. Ground
8. +5 to +12 VDC
9. Ground

This circuit may be used to provide a single Audio and COS output from two radio sources that provide a positive going COS. The primary channel has precedence over the other so that one can be a control channel that removes audio from the secondary channel while control functions are being entered. Only audio from the receiver that is active is present on the output.